


Issue Classification 	Application No.	Applicant(s)	
	10/099,641	CATABAY ET AL.	
	Examiner	Art Unit	
	Lisa A Kilday	2829	

ISSUE CLASSIFICATION											
ORIGINAL					CROSS REFERENCE(S)						
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
257		759			257	774					
INTERNATIONAL CLASSIFICATION											
H	O	I	L	23/48							
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Lisa Kilday 4/26/4 (Assistant Examiner) (Date)					KAMAND CUNEO SUPERVISORY/PATENT EXAMINER TECHNOLOGY CENTER 2800 (Primary Examiner) (Date) 4/26/04					Total Claims Allowed: 5	
Sherry Davis 5/14/04 (Legal Instruments Examiner) (Date)										O.G. Print Claim(s) 15	O.G. Print Fig. 2-3

<input checked="" type="checkbox"/> Claims renumbered in the same order as presented by applicant										<input type="checkbox"/> CPA		<input type="checkbox"/> T.D.		<input type="checkbox"/> R.1.47	
Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original
	1		31		61		91		121		151		181		
	2		32		62		92		122		152		182		
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	5		35		65		95		125		155		185		
	6		36		66		96		126		156		186		
	7		37		67		97		127		157		187		
	8		38		68		98		128		158		188		
	9		39		69		99		129		159		189		
	10		40		70		100		130		160		190		
	11		41		71		101		131		161		191		
	12		42		72		102		132		162		192		
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	29		59		89		119		149		179		209		
	30		60		90		120		150		180		210		

10/099641
03/15/02

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10099641	03/15/2002	438	75-	2829	

****APPLICANTS:** Catabay Wilbur; Schinella Richard;

****CONTINUING DATA VERIFIED:**
THIS APPLICATION IS A DIV OF 09/428,056 10/22/1999 PAT 6,391,795
LAK verified 4/14/03 *LAK verified*
2/26/04

****FOREIGN APPLICATIONS VERIFIED:**
LAK none 12/14/03 *LAK none 4/14/04*

PG-PUB ☐ DO NOT PUBLISH ☐ RESCIND ☐

Foreign priority claimed ☐ yes ☒ no
35 USC 119 conditions met ☐ yes ☒ no
Verified and Acknowledged Examiner's Initials *WRE*

ATTORNEY DOCKET NO
99-102/1D

TITLE : Low k dielectric composite layer for integrated circuit structure which provides void-free low k dielectric material between metal lines while mitigating via poisoning

U.S. DEPT. OF COMM. PAT. & TM. FTO-CAL (Rev. 12-99)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claims for 0.0
Assistant Examiner		DRAWING	
		Sheets Draw.	Figs. Draw. Print Fig.
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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